

## گروه فنی مهندسی جوش و برش مقدم

اعتماد از شما کیفیت و تخصص از ما



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مشهد خیام شمالی 63 خیابان پردیس 3

برای کسب اطلاعات بیشتر بر روی لینک ها کلیک کنید

- 7 سال سابقه آموزش تعمیرات تخصصی دستگاه های جوش اینورتری تک فاز و 3 فاز
- 7 سال سابقه فروش قطعات الکترونیکی دستگاه جوش
   تک فاز و 3 فاز
- آموزش تخصصی تحلیل دستگاه های جوش اینورتری مختص ابراز فروشان
  - آموزش تخصصی ابراز آلات شارژی



# International Rectifier

## IRF3205PbF

## HEXFET® Power MOSFET

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

# G

 $V_{DSS} = 55V$   $R_{DS(on)} = 8.0 \text{m} \Lambda$   $I_{D} = 110 \text{A}^{\textcircled{5}}$ 

#### **Description**

Advanced HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.



## **Absolute Maximum Ratings**

	Parameter	Max.	Units
$I_D @ T_C = 25^{\circ}C$	Continuous Drain Current, V <sub>GS</sub> @ 10V	110	
$I_D @ T_C = 100^{\circ}C$	Continuous Drain Current, V <sub>GS</sub> @ 10V	80	A
I <sub>DM</sub>	Pulsed Drain Current ①	390	
$P_D @ T_C = 25^{\circ}C$	Power Dissipation	200	W
	Linear Derating Factor	1.3	W/°C
$V_{GS}$	Gate-to-Source Voltage	± 20	V
I <sub>AR</sub>	Avalanche Current①	62	А
E <sub>AR</sub>	Repetitive Avalanche Energy①	20	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.0	V/ns
T <sub>J</sub>	Operating Junction and	-55 to + 175	
T <sub>STG</sub>	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case )	
	Mounting torque, 6-32 or M3 srew	10 lbf•in (1.1N•m)	

#### **Thermal Resistance**

	Parameter	Тур.	Max.	Units
R <sub>θ</sub> JC	Junction-to-Case		0.75	
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
$R_{\theta JA}$	Junction-to-Ambient		62	

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## Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	55			V	$V_{GS} = 0V, I_{D} = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.057		V/°C	Reference to 25°C, I <sub>D</sub> = 1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance			8.0	mΛ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 62A ④
V <sub>GS(th)</sub>	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
<b>g</b> fs	Forward Transconductance	44			S	$V_{DS} = 25V, I_{D} = 62A$
I <sub>DSS</sub>	Drain-to-Source Leakage Current			25	μA	$V_{DS} = 55V$ , $V_{GS} = 0V$
1088	Drain to Godino Loanago Garroni			250	μΛ	$V_{DS} = 44V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
I <sub>GSS</sub>	Gate-to-Source Forward Leakage			100	nA	V <sub>GS</sub> = 20V
iGSS	Gate-to-Source Reverse Leakage			-100	IIA	V <sub>GS</sub> = -20V
Qg	Total Gate Charge			146		I <sub>D</sub> = 62A
Q <sub>gs</sub>	Gate-to-Source Charge			35	nC	$V_{DS} = 44V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge			54		$V_{GS}$ = 10V, See Fig. 6 and 13
t <sub>d(on)</sub>	Turn-On Delay Time		14			$V_{DD} = 28V$
t <sub>r</sub>	Rise Time		101		no	$I_D = 62A$
t <sub>d(off)</sub>	Turn-Off Delay Time		50		ns	$R_G = 4.5\Lambda$
t <sub>f</sub>	Fall Time		65			V <sub>GS</sub> = 10V, See Fig. 10 ④
1	Internal Drain Inductance		4.5			Between lead,
L <sub>D</sub>	internal Dialit inductance		4.5		nH	6mm (0.25in.)
Ls	Internal Source Inductance		7.5		ПΠ	from package
						and center of die contact
Ciss	Input Capacitance		3247	_		$V_{GS} = 0V$
Coss	Output Capacitance		781			$V_{DS} = 25V$
C <sub>rss</sub>	Reverse Transfer Capacitance		211		pF	f = 1.0MHz, See Fig. 5
E <sub>AS</sub>	Single Pulse Avalanche Energy 🕔		1050®	2647	mJ	I <sub>AS</sub> = 62A, L = 138μH

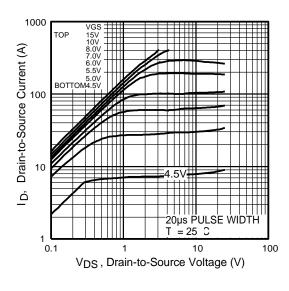
### **Source-Drain Ratings and Characteristics**

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			110		MOSFET symbol
	(Body Diode)			Α	showing the	
I <sub>SM</sub>	Pulsed Source Current			390	, ,	integral reverse
	(Body Diode)①			390		p-n junction diode.
V <sub>SD</sub>	Diode Forward Voltage			1.3	V	$T_J = 25$ °C, $I_S = 62$ A, $V_{GS} = 0$ V 4
t <sub>rr</sub>	Reverse Recovery Time		69	104	ns	$T_J = 25^{\circ}C, I_F = 62A$
Q <sub>rr</sub>	Reverse Recovery Charge		143	215	nC	di/dt = 100A/µs ④
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> +L <sub>D</sub> )				

#### Notes:

- (1) Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )
- Starting  $T_J = 25^{\circ}C$ ,  $L = 138\mu H$  $R_G = 25\Lambda$ ,  $I_{AS} = 62A$ . (See Figure 12)
- $\begin{tabular}{l} \hline (3) & I_{SD} \le 62A, & di/dt \le 207A/\mu s, & V_{DD} \le V_{(BR)DSS}, \\ & T_{J} \le 175^{\circ}C \\ \end{tabular}$
- ④ Pulse width ≤ 400µs; duty cycle ≤ 2%. Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.
- ® This is a typical value at device destruction and represents operation outside rated limits.
- $\ensuremath{\bigcirc}$  This is a calculated value limited to T<sub>J</sub> = 175°C.

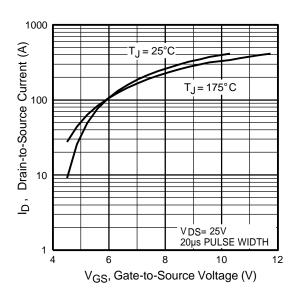
## IRF3205PbF



1000

Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics



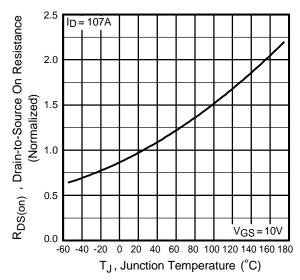
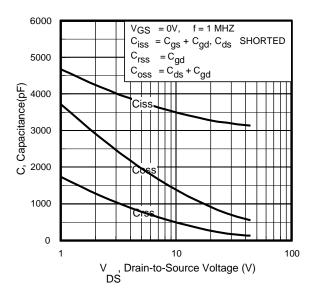


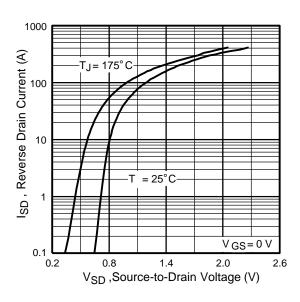
Fig 3. Typical Transfer Characteristics

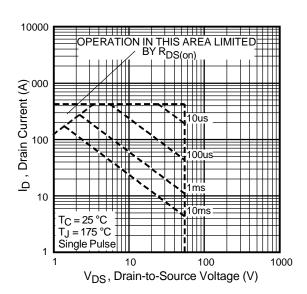
**Fig 4.** Normalized On-Resistance Vs. Temperature



**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage

**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



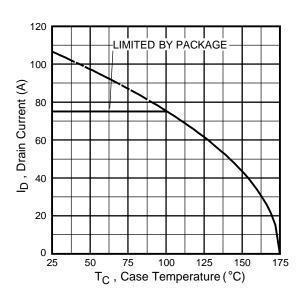


**Fig 7.** Typical Source-Drain Diode Forward Voltage

Fig 8. Maximum Safe Operating Area

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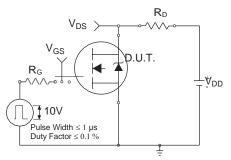
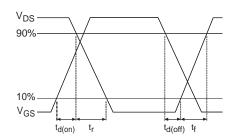


Fig 10a. Switching Time Test Circuit



**Fig 9.** Maximum Drain Current Vs. Case Temperature

Fig 10b. Switching Time Waveforms

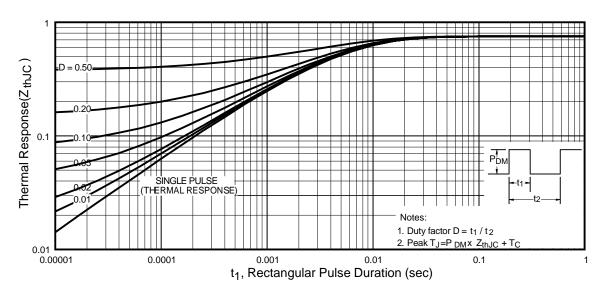


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

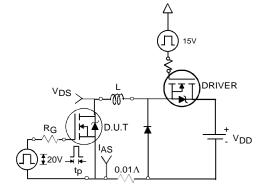


Fig 12a. Unclamped Inductive Test Circuit

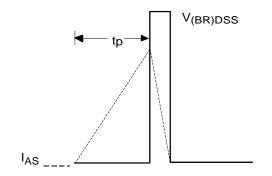


Fig 12b. Unclamped Inductive Waveforms

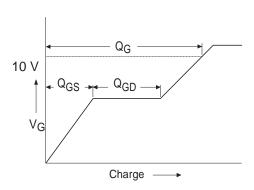
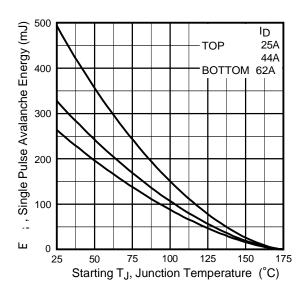


Fig 13a. Basic Gate Charge Waveform



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current

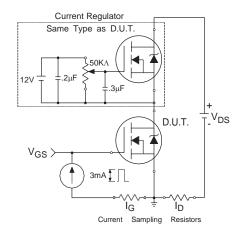
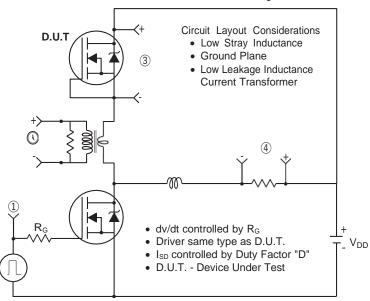


Fig 13b. Gate Charge Test Circuit

## Peak Diode Recovery dv/dt Test Circuit



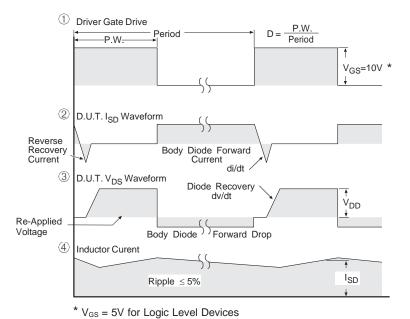


Fig 14. For N-Channel HEXFETS

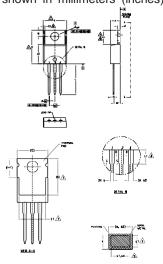
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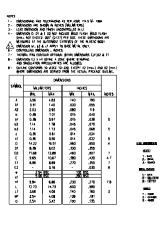
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#### TO-220AB Package Outline

Dimensions are shown in millimeters (inches)





## TO-220AB Part Marking Information

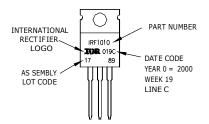
EXAMPLE: THIS IS AN IRF1010

LOT CODE 1789

ASSEMBLED ON WW 19, 2000

IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position indicates "Lead - Free"



TO-220AB package is not recommended for Surface Mount Application

#### Notes:

- 1. For an Automotive Qualified version of this part please seehttp://www.irf.com/product-info/auto/
- 2. For the most current drawing please refer to IR website at http://www.irf.com/package/

Data and specifications subject to change without notice. This product has been designed and qualified for the Industrial market.

Qualification Standards can be found on IR's Web site.



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