

گروه فنی مهندسی جوش و برش مقدم

اعتماد از شما کیفیت و تخصص از ما



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مشهد خیام شمالی 63 خیابان پردیس 3



- 7 سال سابقه آموزش تعمیرات تخصصی دستگاه های جوش اینورتری تک فاز و 3 فاز
- 7 سال سابقه فروش قطعات الکترونیکی دستگاه جوش
 تک فاز و 3 فاز
 - آموزش تخصصی تحلیل دستگاه های جوش اینورتری مختص ابراز فروشان
 - آموزش تخصصی ابراز آلات شارژی



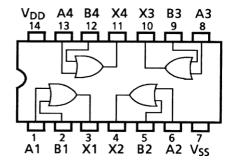
TC4071BP, TC4071BF

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TC4071B Quad 2-Input OR Gate

TC4071B is positive logic OR gates with two inputs respectively. As all the outputs gates are equipped with the buffer circuits of inverters, the input/output propagation characteristic has been improved and the variation of propagation time caused by increase of load capacity is kept minimum.

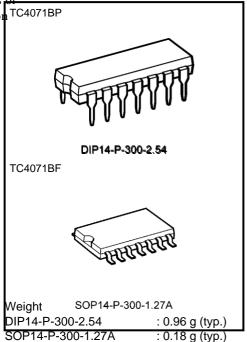
Pin Assignment



Logic Diagram

1/4 TC4071B





Start of commercial production 1978-06

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	Vss - 0.5 to Vss + 20	V
Input voltage	V _{IN}	Vss - 0.5 to V _{DD} + 0.5	V
Output voltage	V _{OUT}	$V_{SS}-0.5$ to $V_{DD}+0.5$	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Vss = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V_{IN}	_	0	_	V_{DD}	V

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Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 \ V$)

Characteristics S		Sym-	Test Condition		-40°C		25°C			85°C		
		bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output Voltage			I _{OUT} < 1 μA	5	4.95	_	4.95	5.00	_	4.95	_	
		V_{OH}	$V_{IN} = V_{SS}, V_{DD}$	10	9.95	_	9.95	10.00	_	9.95	_	V
			VIN - VSS, VDD	15	14.95	_	14.95	15.00	_	14.95	_	
			I _{OUT} < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level voltage	output	V _{OL}	·	10	_	0.05	_	0.00	0.05	_	0.05	V
			$V_{IN} = V_{SS}, V_{DD}$	15	_	0.05	_	0.00	0.05	_	0.05	
			VOH = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA
Output hig	h current	I _{OH}	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
			VoH = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
			VoL = 0.4 V	5	0.61	_	0.51	1.5	_	0.42	_	
Output lov	, allerant		VoL = 0.5 V	10	1.50	_	1.30	3.8	_	1.10	_	mA
Output low current		l _{OL}	V _{OL} = 1.5 V	15	4.00	_	3.40	15.0	_	2.80	_	IIIA
			$V_{IN} = V_{SS}$									
		V _{IH}	Vout = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	V
Lance of Indian	11		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
Input high	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	
			I _{OUT} < 1 μA									
			Vout = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
	V _{IL}	V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V	
Input low voltage		V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0		
			$ I_{OUT} < 1 \mu A$									
Input	"H" level	I _{IH}	V _{IH} = 18 V	18	_	0.1	_	10 ⁻⁵	0.1	_	1.0	
current	"L" level	I _{IL}	VIL = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ
				5	_	0.25	_	0.001	0.25	_	7.5	
Quiescent current	Quiescent supply current		$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	0.50	_	0.001	0.50	_	15.0	μА
			(14010)	15		1.00		0.002	1.00	_	30.0	

Note: All valid input combinations

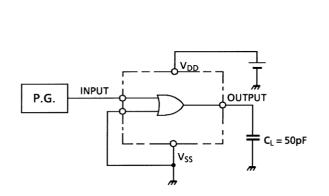
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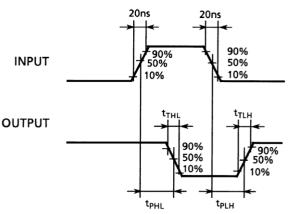
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test Condition	Min	Tun	Max	Unit	
Cildiacteristics	Symbol		V _{DD} (V)	IVIIII	Тур.	IVIAX	Offic
Output transition time			5	_	70	200	
Output transition time	t _{TLH}	_	10	_	35	100	ns
(low to high)			15	_	30	80	
Output transition time			5	_	70	200	
	t _{THL}	_	10	_	35	100	ns
(high to low)			15		30	80	
	t _{pLH}	_	5	_	65	200	
Propagation delay time			10	_	30	100	ns
			15		25	80	
	t _{pHL}	_	5	_	65	200	
Propagation delay time			10	_	30	100	ns
			15	_	25	80	
Input capacitance	C _{IN}	_		_	5	7.5	pF

Circuit and Waveform for Measurement of Dynamic Characteristics

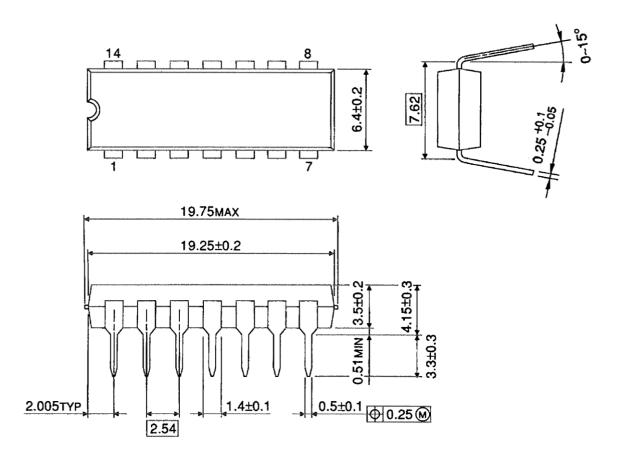
Circuit Waveform





Package Dimensions

DIP14-P-300-2.54 Unit: mm

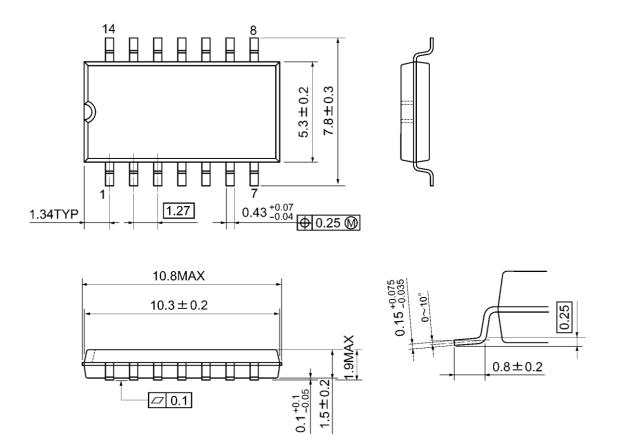


Weight: 0.96 g (typ.)

5 2014-03-01

Package Dimensions

SOP14-P-300-1.27A Unit: mm



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Weight: 0.18 g (typ.)

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